

Attorney Docket No.: MI/242

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Kobayashi et al

Serial No.: 10/575,344

Filed: April 7, 2006

For: Electrode For Electric Double Layer

Capacitor, Method For Manufacturing of the Same, Electric Double Layer Capacitor

and Conductive Adhesive

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Date: October 5, 2006

) Group Art Unit:) Examiner:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

M. Manague

October 5, 2006

Date of Faxing Document

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R §1.56 and in accordance with 37 C.F.R. §§1.97-1.98, information relating to the above-identified application is hereby disclosed. Inclusion of information in this statement is not to be construed as an admission that this information is material as that term is defined in 37 C.F.R. §1.56(b).

In accordance with §1.97(b), since this Supplemental Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, within three months of the date of entry into the national stage of the above-identified application as set forth in §1.491, or before the mailing date of a first Office Action on the merits of the above-identified application, no additional fee is required.

A copy of the foreign reference listed on the attached Form PTO-1449 is enclosed herewith.

Respectfully submitted,

Allan M. Wheatdraft 36,307

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